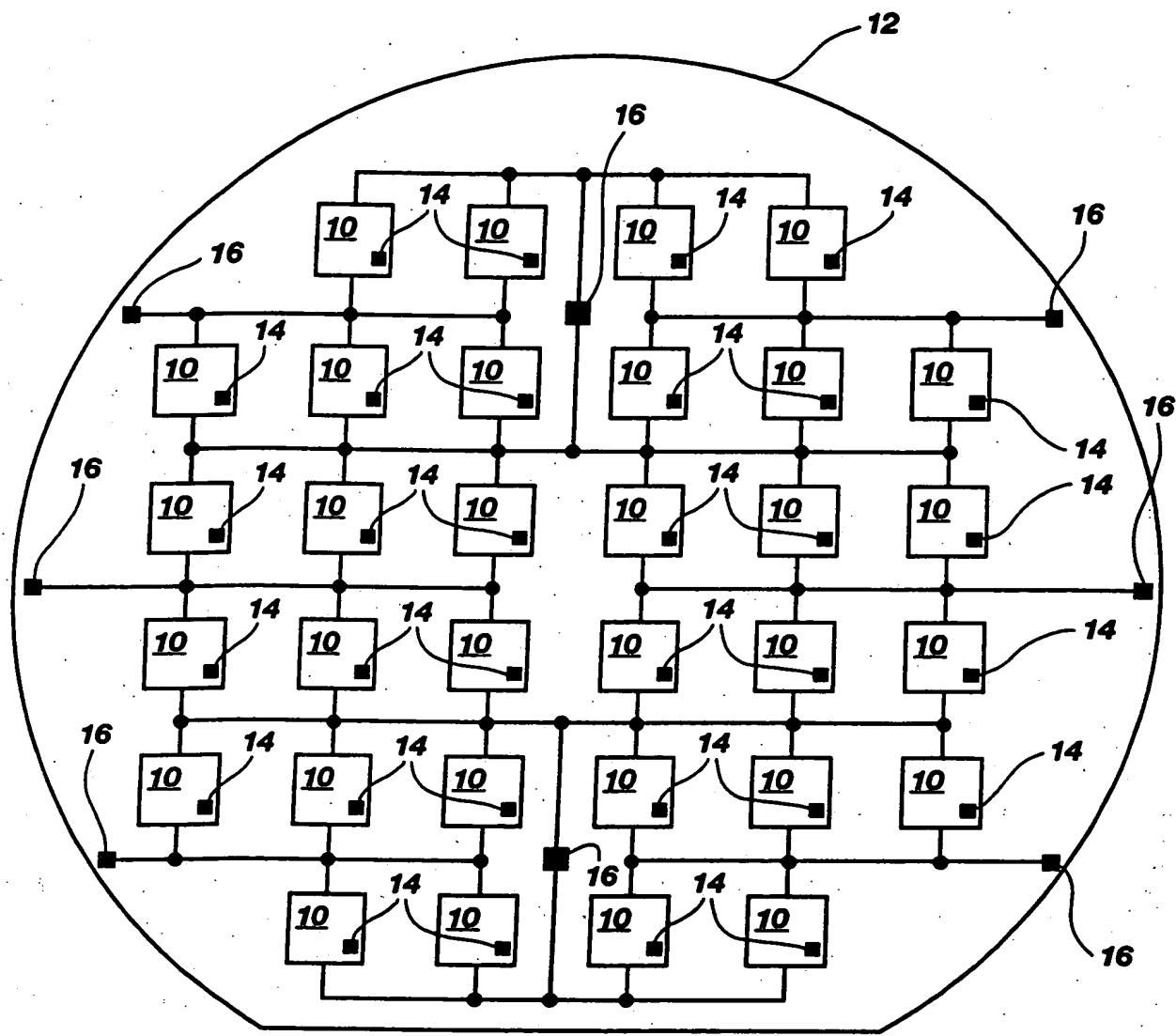


TITLE: DEVICE AND METHOD FOR ISOLATING A SHORT-CIRCUITED INTEGRATED CIRCUIT (IC) FROM OTHER ICs ON A SEMICONDUCTOR WAFER  
Inventor: Farnsworth et al.  
Docket No.: 3038.3US

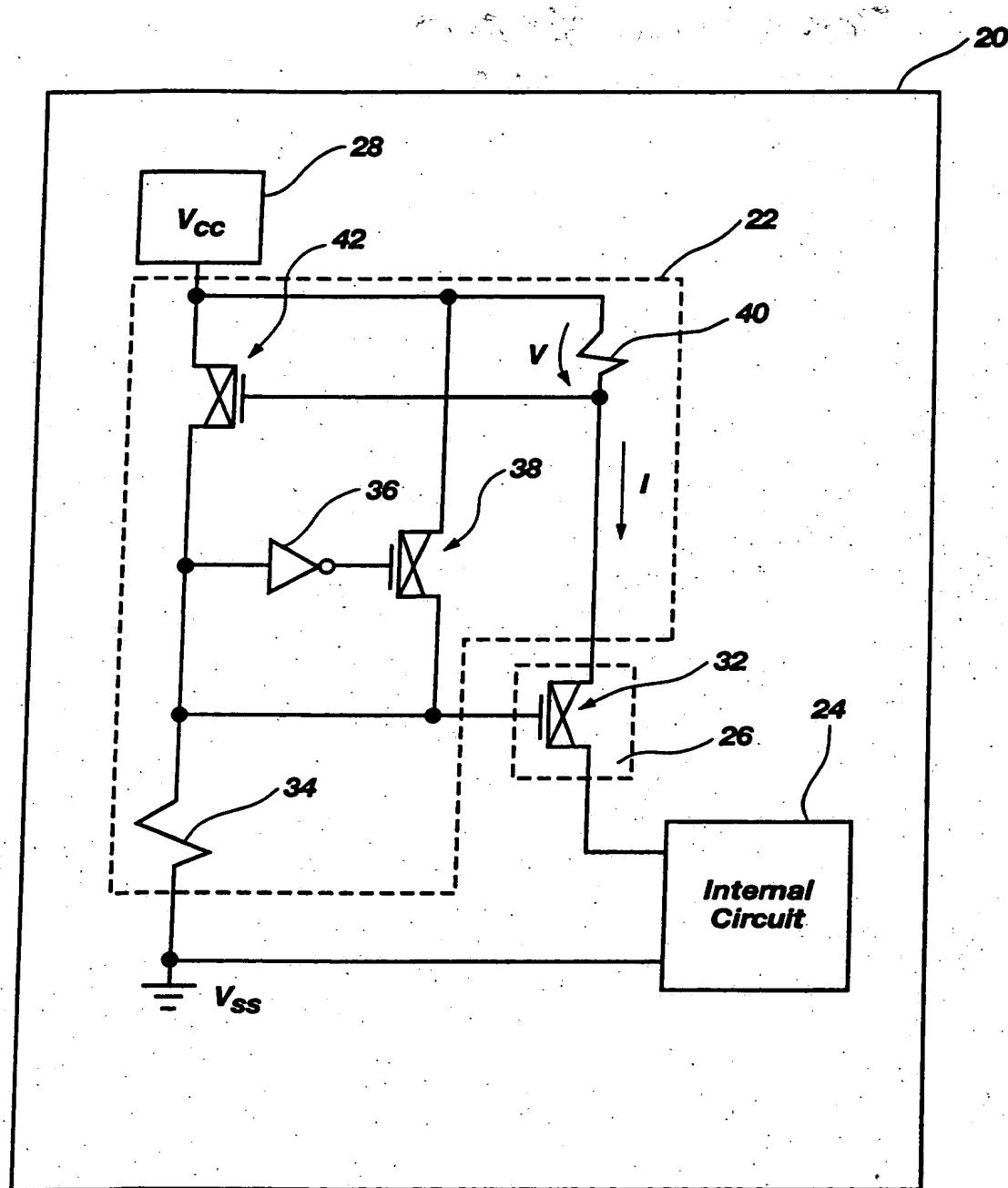
1/6



**Fig. 1**  
**(PRIOR ART)**

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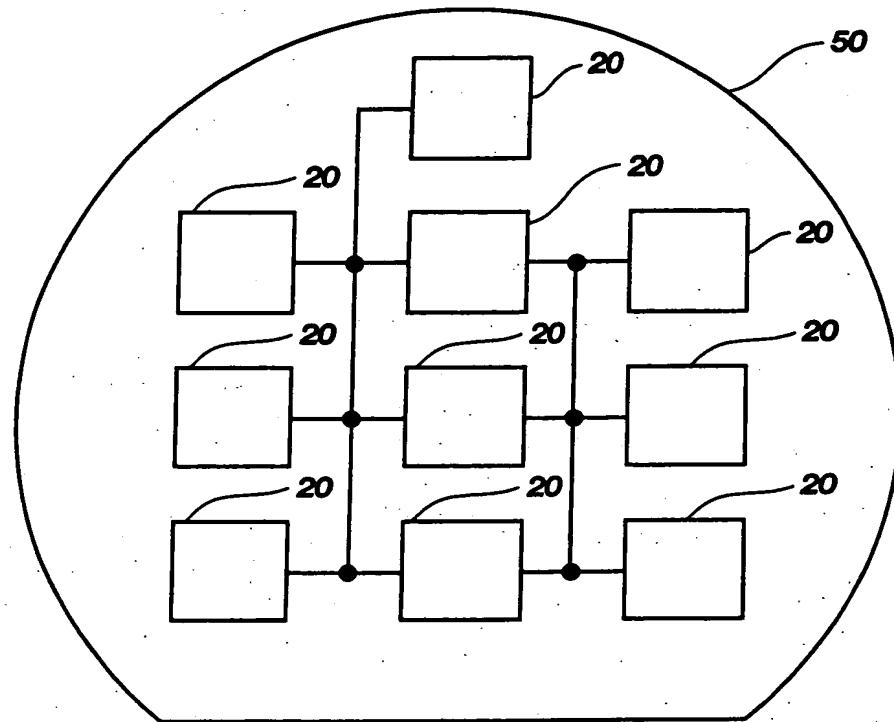
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**Fig. 2**

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Inventor: Farnworth et al.  
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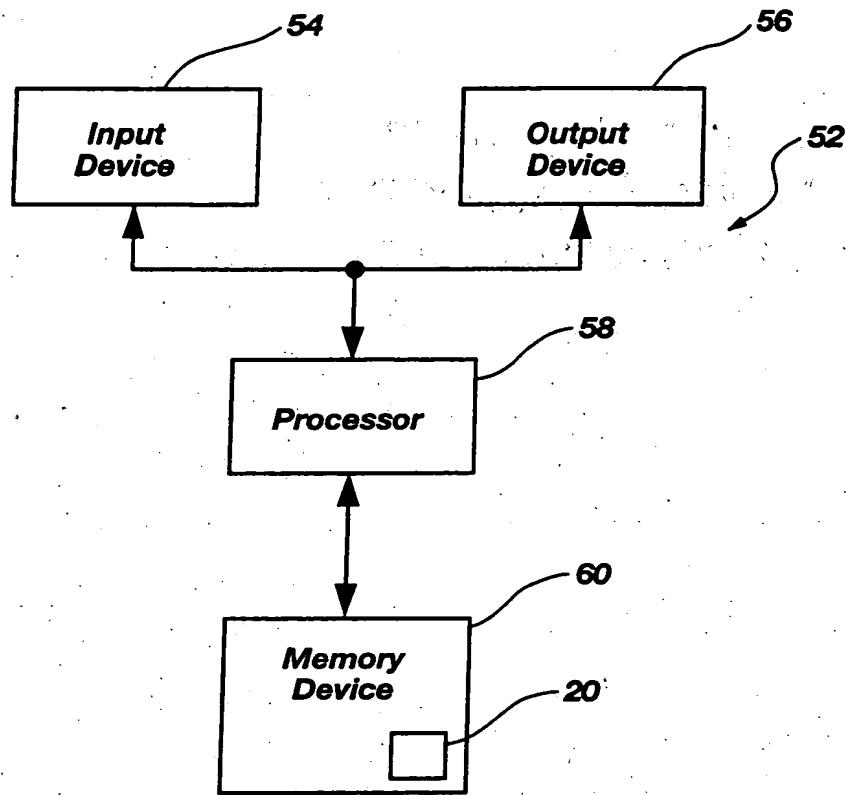
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***Fig. 3***

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Inventor: Farnsworth et al.  
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**Fig. 4**

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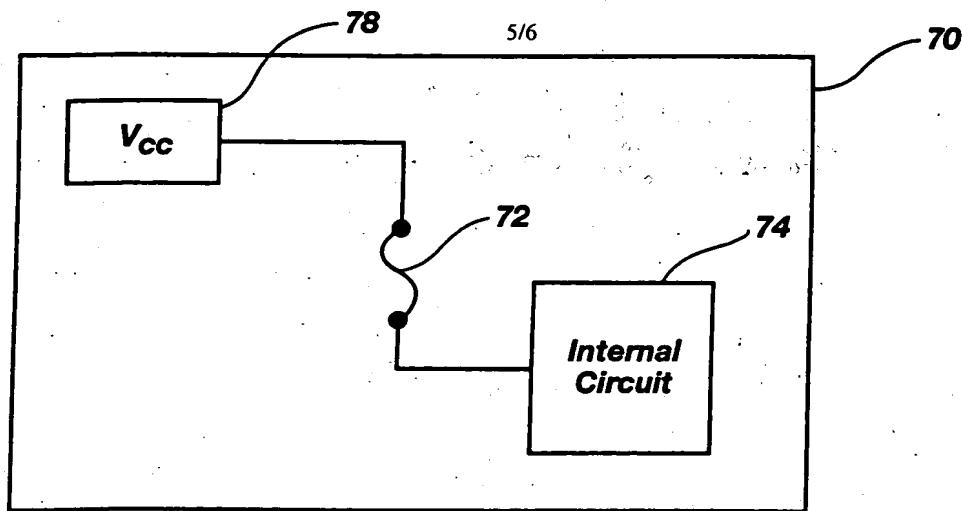


Fig. 5

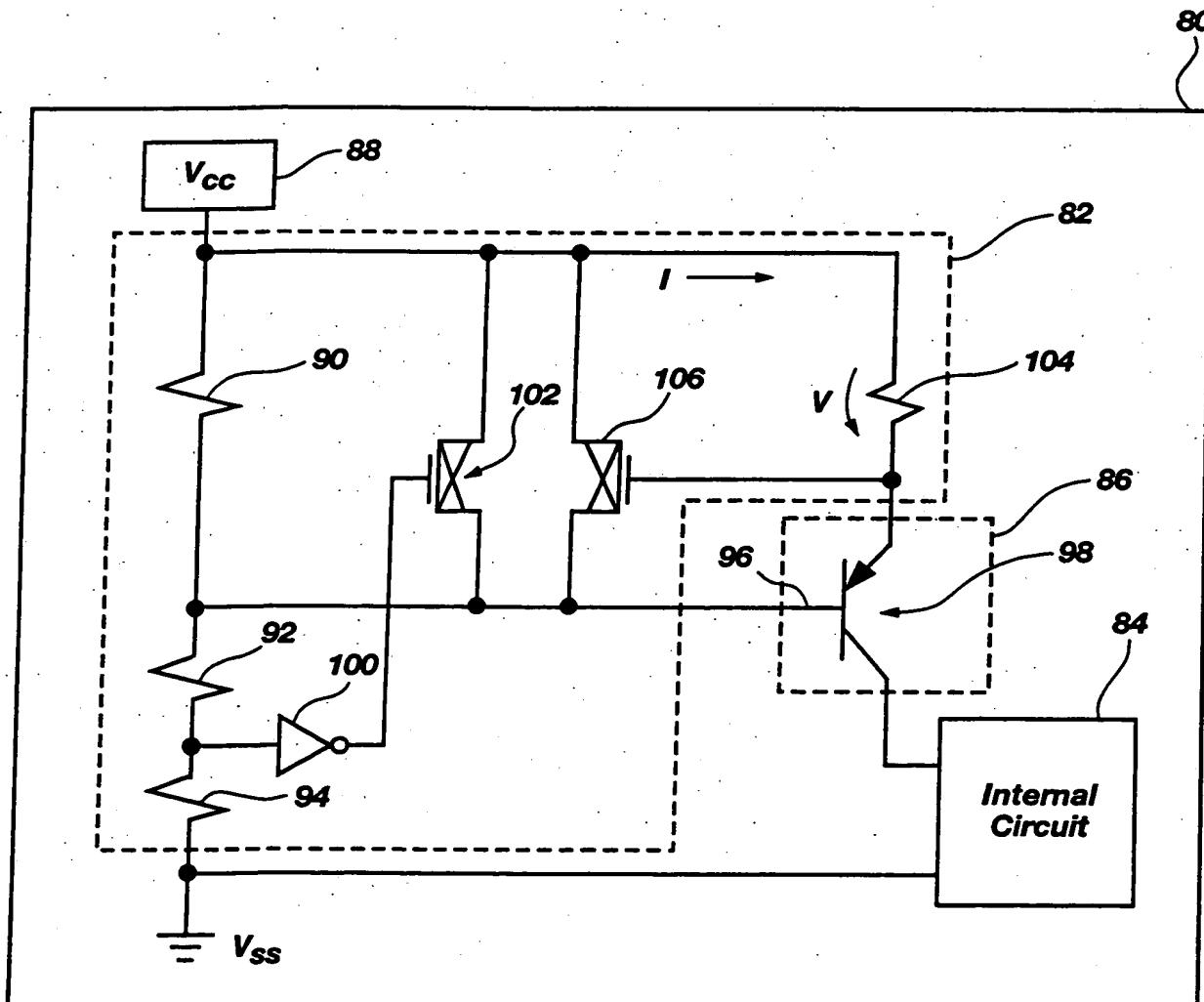
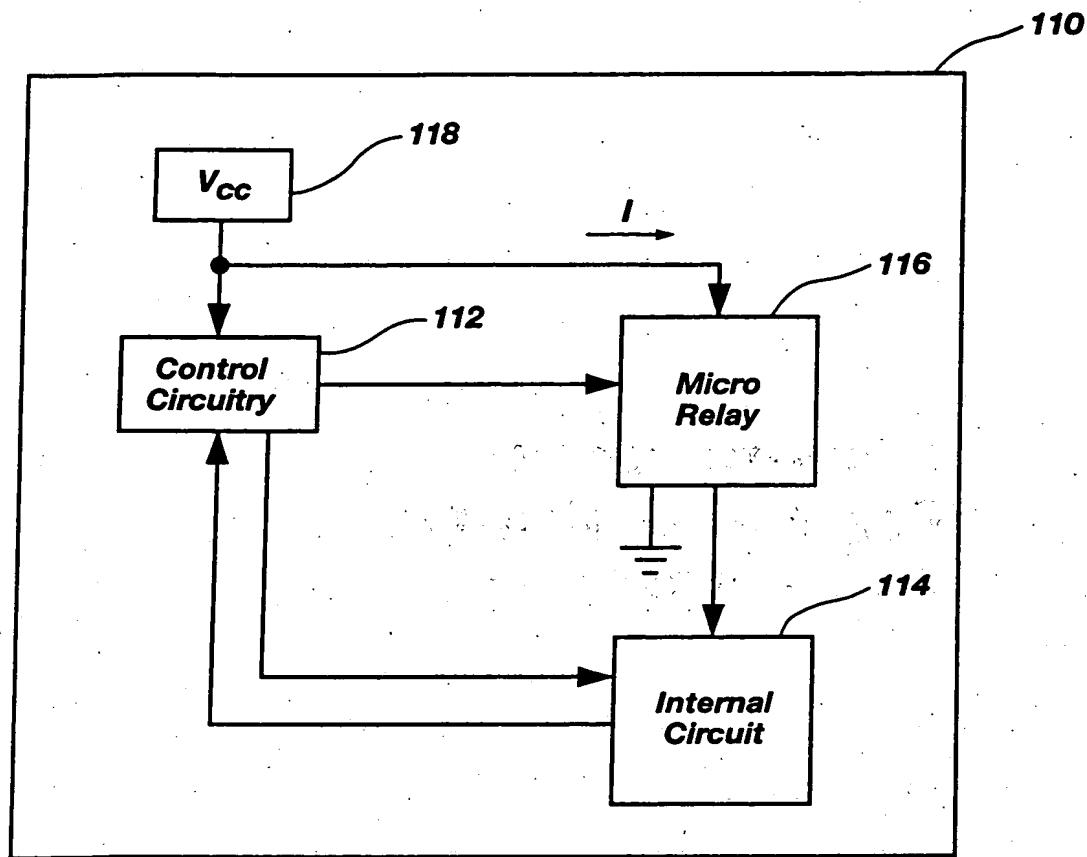


Fig. 6

TITLE: DEVICE AND METHOD FOR ISOLATING A SHORT-CIRCUITED INTEGRATED CIRCUIT (IC) FROM OTHER ICs ON A SEMICONDUCTOR WAFER

Inventor: Farnsworth et al.  
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**Fig. 7**